E91
Embedded System

Intro
Why embedded systems?

• Big bang-for-the-buck by adding some intelligence to systems.
• Embedded Systems are ubiquitous.
• Embedded Systems more common as prices drop, and power decreases.
Which Embedded System?

• We will use Texas Instruments MSP-430
  + very popular (#2 next to microchip)
  + 16 bits (instead of 8)
  + low power
  + clean architecture
  + low cost (free) development tools
  - relatively low speed/capacity (i.e., no video or fancy audio)
  - low level tools (compared to Stamp, Arduino...)
  - 16 bits (instead of 32)
Course overview

- Mix of hardware and software
- Mostly C, some assembly
- Lots of information, less conceptual than many courses.
- Two hardware kits, EZ430 and the 430 Experimenter’s Board
- About 2 hours of lecture per week.
- More hours in lab – writeups will be short.
- In lab you will need to become familiar with a broad set of documentation. I will try to keep a complete list on website. Let me know if you find good documents I have not listed.
- You will need to be more self-reliant than in many courses (though I am always ready to help).
- Maybe some assignments.
Policy on working together:

• Lab groups should be 2 (preferably) or 3 students.
• We expect labs to be done as a group with your lab partners. You may discuss your lab with other groups, but you may not copy anything from their reports. Each group will submit a single report (with all members of the group listed).
Today

• Brief overview of
  o logic
  o numbers
  o C
  o MSP430 digital I/O

Today’s lecture will be very densely packed so you can start on the lab. We will come back and repeat some of the material in more depth as needed.
Brief Review of Logic

• In this class we will deal with logical circuits (i.e., inputs are either true (logical 1, for us this is 3.3V) or false (logical 0, for us this is 0V). In class you will learn why this is useful, for now, accept it.

• To deal with these signal levels we develop a special form of mathematics, Boolean algebra.
Boolean Operators

- Operators:
  - AND: \( C = A \cdot B \) (Read as C equals A and B).

<table>
<thead>
<tr>
<th>Input A</th>
<th>Input B</th>
<th>Output C</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

“Truth Table”

Sometimes the “∙” is dropped: \( C = AB \)

logic gate images adapted from:  http://www.ee.surrey.ac.uk/Projects/Labview/gatesfunc/index.html
More logic...

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>D</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>A</th>
<th>F</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>H</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>
... and even more.

Note:
XOR with 1 inverts bit,
XOR with 0 passes bit.
Number Systems

**Binary:** $00001101_2 = 1\cdot2^3 + 1\cdot2^2 + 0\cdot2^1 + 1\cdot2^0 = 8 + 4 + 1 = 13$

**Hex:** $00101010_2 = 2A_{16} = 0\times2A = 2\cdot16^1 + 10\cdot16^0 = 32 + 10 = 42$
(check $1\cdot2^5 + 1\cdot2^3 + 2\cdot2^1 = 32 + 8 + 2 = 42$)

**8 bits = 1 byte**

$0000\ 0000_2 \rightarrow 1111\ 1111_2$
$0x00 \rightarrow 0xff$

$0 \rightarrow 2^8-1=255 \ (\text{or} \ -128 \rightarrow 127 \ (-(2^7) \rightarrow 2^7-1)))$

**16 bits = 2 bytes = 1 word**

$0000\ 0000\ 0000\ 0000_2 \rightarrow 1111\ 1111\ 1111\ 1111_2$
$0x0000 \rightarrow 0xffff$

$0 \rightarrow 2^{16-1}=65535 \ (\text{or} \ -32768 \rightarrow 32767 \ (-2^{15}) \rightarrow 2^{15-1})))$

**4 bits = 1 nybble** \ (0 \rightarrow 2^4-1=15)
#include <msp430x20x3.h>

void main(void) {
    volatile int i;
    WDTCTL = WDTPW + WDTHOLD; // Stop watchdog timer
    P1DIR |= 0x01;            // Set P1.0 to output direction
    while (1) {  // Do this forever
        P1OUT = P1OUT | 0x01;         // Set P1.0 with "or", |
        for (i=0; i<0x5000; i++) {} // Delay
        P1OUT = P1OUT & ~0x01;       // Clear P1.0
        for (i=0; i<0x5000; i++) {} // Delay
    }
}

Don’t worry about for now.

Set bit 0 high (connected to LED)
Set bit 0 low (LED turns off)
Loop to waste time
Loop to waste time

Comments start with “//” and go to end of line.
Also note that every statement ends with “;” or “}”
A typical I/O pin

- Analog switch
- Inverted input
- Electronically controlled digital switch
- 2-1 Mux (multiplexer)
- Tri-State Output
- Schmitt trigger (hysteresis)
- Transparent latch
Effect of P1DIR

Port P1 (P1.0) pin schematics, MSP430x20x3

Latch is disabled

0, switch is open
**Variant 1 (more readable)**

```c
#include <msp430x20x3.h>
debug LED 0x01

give constants meaningful names.

void main(void) {
    volatile int i;
    // Stop watchdog timer
    WDTCTL = WDTPW + WDTHOLD;
    P1DIR |= LED; // Set P1.0 (LED bit) to output

    // Do this forever
    while (1) {
        P1OUT |= LED; // Turn on LED
        for (i = 0; i < 0x5000; i++) {} // Delay
        P1OUT &= ~0x01; // Turn off LED
        for (i = 0; i < 0x5000; i++) {} // Delay
    }
}
```

Equivalent to: `P1OUT = P1OUT | LED;`
#include <msp430x20x3.h>
define LED 0x01
#define SETBIT(p,b) ((p) |= (b))
define CLRBIT(p,b) ((p) &= ~(b))

void main(void) {
    volatile int i;
    WDTCTL = WDTPW + WDTHOLD; // Stop watchdog timer
    P1DIR |= LED; // Set P1.0 to output direction

    while (1) { // Do this forever
        SETBIT(P1OUT,LED); // Set P1.0
        for (i=0; i<0x5000; i++) {} // Delay
        CLRBIT(P1OUT,LED); // Clear P1.0
        for (i=0; i<0x5000; i++) {} // Delay
    }
}

Use Macros sparingly, but they can make code look much cleaner (see below)

Expands to: (P1OUT |= (0x01))
Note “;” must be added.
Variant 3 (shorter)

```c
#include <msp430x20x3.h>
define LED 0x01
#define SETBIT(p,b) (p |= (b))
define CLRBIT(p,b) (p &= ~(b))
define TGLBIT(p,b) (p ^= (b))

volatile int i;
WDTCTL = WDTPW + WDTHOLD; // Stop watchdog timer
P1DIR |= 0x01;               // Set P1.0 to output direction

while (1) {  //Do this forever
    TGLBIT(P1OUT,LED);        // Toggle LED
    for (i=0; i<0x5000; i++) {}  // Delay
}
```

- New macro to toggle (xor) bit
- Loop is half as long as before
## C Data Types

(that we will use)

<table>
<thead>
<tr>
<th>Type</th>
<th>Size (bits)</th>
<th>Representation</th>
<th>Minimum</th>
<th>Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>char, signed char</td>
<td>8</td>
<td>ASCII</td>
<td>-128</td>
<td>+127</td>
</tr>
<tr>
<td>unsigned char, bool</td>
<td>8</td>
<td>ASCII</td>
<td>0</td>
<td>255</td>
</tr>
<tr>
<td>int, signed int</td>
<td>16</td>
<td>2s complement</td>
<td>-32 768</td>
<td>32 767</td>
</tr>
<tr>
<td>unsigned int</td>
<td>16</td>
<td>Binary</td>
<td>0</td>
<td>65 535</td>
</tr>
<tr>
<td>long, signed long</td>
<td>32</td>
<td>2s complement</td>
<td>-2 147 483 648</td>
<td>2 147 483 647</td>
</tr>
<tr>
<td>unsigned long</td>
<td>32</td>
<td>Binary</td>
<td>0</td>
<td>4 294 967 295</td>
</tr>
<tr>
<td>enum</td>
<td>16</td>
<td>2s complement</td>
<td>-32 768</td>
<td>32 767</td>
</tr>
<tr>
<td>float</td>
<td>32</td>
<td>IEEE 32-bit</td>
<td>±1.175 495e-38</td>
<td>±3.40 282 35e+38</td>
</tr>
</tbody>
</table>
# C Operators

(Arithmetic)

<table>
<thead>
<tr>
<th>Arithmetic Operator name</th>
<th>Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic assignment</td>
<td>a = b</td>
</tr>
<tr>
<td>Addition</td>
<td>a + b</td>
</tr>
<tr>
<td>Subtraction</td>
<td>a - b</td>
</tr>
<tr>
<td>Unary plus</td>
<td>+a</td>
</tr>
<tr>
<td>Unary minus (additive inverse)</td>
<td>-a</td>
</tr>
<tr>
<td>Multiplication</td>
<td>a * b</td>
</tr>
<tr>
<td>Division</td>
<td>a / b</td>
</tr>
<tr>
<td>Modulo (remainder)</td>
<td>a % b</td>
</tr>
<tr>
<td>Increment</td>
<td>Prefix</td>
</tr>
<tr>
<td></td>
<td>Suffix</td>
</tr>
<tr>
<td>Decrement</td>
<td>Prefix</td>
</tr>
<tr>
<td></td>
<td>Suffix</td>
</tr>
</tbody>
</table>
# More C Operators
(Relational, Logical, Bitwise and Compound)

## Relational Operator

<table>
<thead>
<tr>
<th>Operator Name</th>
<th>Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>Equal to</td>
<td>a == b</td>
</tr>
<tr>
<td>Not equal to</td>
<td>a != b</td>
</tr>
<tr>
<td>Greater than</td>
<td>a &gt; b</td>
</tr>
<tr>
<td>Less than</td>
<td>a &lt; b</td>
</tr>
<tr>
<td>Greater than or equal to</td>
<td>a &gt;= b</td>
</tr>
<tr>
<td>Less than or equal to</td>
<td>a &lt;= b</td>
</tr>
</tbody>
</table>

## Logical Operator

<table>
<thead>
<tr>
<th>Name</th>
<th>Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logical negation (NOT)</td>
<td>!a</td>
</tr>
<tr>
<td>Logical AND</td>
<td>a &amp;&amp; b</td>
</tr>
<tr>
<td>Logical OR</td>
<td>a</td>
</tr>
</tbody>
</table>

## Compound Operator

<table>
<thead>
<tr>
<th>Name</th>
<th>Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>Addition assignment</td>
<td>a += b</td>
</tr>
<tr>
<td>Subtraction assignment</td>
<td>a -= b</td>
</tr>
<tr>
<td>Multiplication assignment</td>
<td>a *= b</td>
</tr>
<tr>
<td>Division assignment</td>
<td>a /= b</td>
</tr>
<tr>
<td>Modulo assignment</td>
<td>a %= b</td>
</tr>
<tr>
<td>Bitwise AND assignment</td>
<td>a &amp;= b</td>
</tr>
<tr>
<td>Bitwise OR assignment</td>
<td>a</td>
</tr>
<tr>
<td>Bitwise XOR assignment</td>
<td>a ^= b</td>
</tr>
<tr>
<td>Bitwise left shift assignment</td>
<td>a &lt;&lt;= b</td>
</tr>
<tr>
<td>Bitwise right shift assignment</td>
<td>a &gt;&gt;= b</td>
</tr>
</tbody>
</table>
More C

Statements
• a **simple statement** is a single statement that ends in a “;”
• a **compound statement** is several statements inside braces:

```c
{  
simple statement;
    ...
    simple statement;
}
```

Indenting
There are no rules about indenting code, but if you don’t adopt a standard style, your code becomes unreadable.

```c
while (x == y) {  
something();  
somethingelse();  
if (some_error)  
    do_correct();  
else  
    continue_as_usual();
}
```

```c
while (x == y)  
{  
something();  
somethingelse();  
}  
finalthing();
```

```c
if (x < 0)  
{  
    printf("Negative");  
    negative(x);
}  
else  
{  
    printf("Positive");  
    positive(x);
}  ```
Even more C

Array definition
int a[100];  //Array elements are a[0] to a[99]. Don’t use a[100]!

if...then
if (<expression>)
    <statement>
<statement> may be a compound statement.

if...then...else
if (<expression>)
    <statement1>
else
    <statement2>
Yet more C

Iteration (do...while while... for...)

- do
  - <statement>
  - while ( <expression> );

- while ( <expression> )
  - <statement>

- for ( <expression> ; <expression> ; <expression> )
  - <statement>

Recall: for (i=0; i<0x5000; i++) {} // Delay

- for (e1; e2; e3)
  - s;

is equivalent to

- e1;
- while (e2) {
  - s;
  - e3;
}

The break statement is used to end a for loop, while loop, do loop, or switch statement. Control passes to the statement following the terminated statement.
Again with the C

switch (one choice of many)

switch (<expression>) {
    case <label1> :
        <statements 1>
    case <label2> :
        <statements 2>
        break;
    default :
        <statements 3>
}

• <expression> is compared against the label, and execution of the associated statements occur (i.e., if <expression> is equal to <label1>, <statements 1> are executed.
• No two of the case constants may have the same value.
• There may be at most one default label.
• If none of the case labels are equal to the expression in the parentheses following switch, control passes to the default label, or if there is no default label, execution resumes just beyond the entire construct.
• Switch statements can "fall through", that is, when one case section has completed its execution, statements will continue to be executed downward until a break; statement is encountered. This is usually not wanted, so be careful
Material taken from:

- [http://focus.ti.com/lit/ug/slau144e/slau144e.pdf](http://focus.ti.com/lit/ug/slau144e/slau144e.pdf)
- [http://focus.ti.com/lit/ds/slas491e/slas491e.pdf](http://focus.ti.com/lit/ds/slas491e/slas491e.pdf)